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This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Application Number: 10/084,803

Filing Date:

02/28/2002

Applicant:

Charles Douglas Murphy

Title:

SHARED PARALLÉL DIGITAL-TO-ANALOG CONVERSION

Examiner/GAU:

Jean Bruner Jeanglaude

May 2, 2003

AMENDMENT A

Mail Stop Non-Fee Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed April 21, 2003, please amend the above application as follows:

Specification:

p. 27 - Replace the Abstract with the following paragraph:

A digital-to-analog converter in which a counter tracks the elapsed time since the beginning of a conversion cycle. Simultaneously, a reference analog signal such as a voltage ramp is generated. When the count reaches a stored digital number value, a sample-and-hold circuit acquires the value of the reference analog signal. Multiple stored digital number values can be converted using multiple sample-and-hold circuits which share the counter and the reference analog signal. Such parallel digital-to-analog conversion is useful in many applications such as digital communications, image display, and shared parallel analog-to-digital conversion.

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